

General Co-Chairs

Pingzhi Fan, Southwest Jiaotong University, China Tetsuya Kojima, NIT, Tokyo College, Japan

General Secretary

Takahiro Matsumoto, Yamaguchi University, Japan

Technical Program Co-Chairs

Hideyuki Torii, Kanagawa Inst. of Technology, Japan Xiaohu Tang, Southwest Jiaotong University, China Udaya Parampalli, The Univ. of Melbourne, Australia

Technical Program Committee

Serdar Boztas, RMIT University, Australia Claude Carlet, .AN+ LAGA, University of Paris 8 N, France

Chi-Chao Chao, National Tsing Hua University, Taiwan Qingchun Chen, Southwest Jiaotong University, China Cunsheng Ding, HKUST, Hong Kong Guang Gong, University of Waterloo, Canada Hiromasa Habuchi, Ibaraki University, Japan Chenggao Han, Univ. of Electro-Communications, Japan

Yunghsiang S. Han, NTUST, Taiwan Li Hao, Southwest Jiaotong University, China Tor Helleseth, University of Bergen, Norway Honggang Hu, University of Science and Tech., China Kwang Soon Kim, Yonsei University, Korea Andrew Klapper, University of Kentucky, USA Hsiao-feng Lu, National Chiao Tung University, Taiwan Subhamoy Maitra, Indian Statistical Institute, India Shinya Matsufuji, Yamaguchi University, Japan Kiichi Miyanaga, Hokkaido University, Japan Wai Ho Mow, HKUST, Hong Kong Alexander Pott, Otto-von-Guericke-Univ., Germany Dong-Joon Shin, Hanyang University, Korea Patrick Sole, CNRS-I3S, France Hong-Yeop Song, Yonsei University, Korea Srinivas Talabattula, Indian Institute of Science, India Satoshi Uehara, University of Kitakyushu, Japan Dianhua Wu, Guangxi Normal University, China Kyeongcheol Yang, POSTECH, Korea Xiangyong Zeng, Hubei University, China Junru Zheng, Kyushu Women's University, Japan Zhengchun Zhou, Southwest Jiaotong University, China

Publication

Shunsuke Araki, Kyushu Institute of Technology, Japan

Finance

Takeru Miyazaki, University of Kitakyushu, Japan

Publicity

Yuta Ida, Yamaguchi University, Japan

Local Arrangement

Naofumi Aoki, Hokkaido University, Japan

The Eighth International Workshop on Signal Design and Its Applications in Communications

September 24-28, 2017, Sapporo, Japan

http://www.ca.csse.yamaguchi-u.ac.jp/IWSDA17/

Call for Papers

The eighth International Workshop on Signal Design and its Applications in Communications (IWSDA'17) will be held at *Hokkaido University*, Sapporo, Japan on September 24 – 28, 2017. The IWSDA workshop series dates back to September 2001 in Chengdu, China, and since 2005 has been held every 2 years in Yamaguchi, Japan, Chengdu, China, Fukuoka, Japan, Guilin China, Tokyo, Japan and Bengaluru, India. The aim of the workshop series is to foster fruitful interactions among signal designers, coding theorists, cryptographic researchers and communications practitioners from all over the world.

Topics

The topics of this workshop include, but are not limited to, the topics listed below:

- Big Data Analysis
- Coding for Communications and Storage
- Coding Theory
- Combinatorial Structures for Code and Sequence Design
- Communication Theory
- Complexity and Computation Theory
- Compressive Sensing and Sparsity

- Cryptography, Privacy and Security
- Data Hiding and Steganography
- Detection and Estimation
- Network Coding and Applications
- Sequences
- Signal Design for Radar
- Signal Processing
- Space-Time Codes
- Wireless Communication and Networks

Important Dates

✓ Manuscript submission:

✓ Acceptance notification:

✓ Camera-ready submission:

✓ Early-Bird registration:

April 28, 2017

June 30, 2017 July 28, 2017

August 11, 2017

Paper Submission

Papers presenting new results in the above areas are hereby solicited. Only electronic submissions via the conference website in PDF formats are accepted. Each submission must be at most 5 pages in length and conform to the double-column IEEE conference proceedings format (style file and templates can be downloaded from the conference website).

Conference Proceedings

It is planned to have the conference proceedings of IWSDA'17 published by *IEEE Press*, and included in *IEEE Xplore*. As before, it is also planned to publish the selected papers of IWSDA'17 in the special section of *IEICE Transactions on Fundamentals* (pending approval).

Sponsors

The workshop is planned to be supported by the following technical sponsors (pending approval): IEEE(USA) / IEEE Sapporo Section (Japan) / Engineering Sciences Society (ESS), IEICE(Japan) / Yamaguchi University (Japan) / National Institute of Technology, Tokyo College (Japan)